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<u>L13</u>	13 and 112	42	<u>L13</u>
<u>L12</u>	14 and L11	222	<u>L12</u>
<u>L11</u>	19 or 110	3981	<u>L11</u>
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<u>L8</u>	14 and 17	91	<u>L8</u>
<u>L7</u>	13 with 16	433	<u>L7</u>
<u>L6</u>	fifo	44490	<u>L6</u>

<u>L5</u>	l3 and l4	2138	<u>L5</u>
<u>L4</u>	l1 near3 l2	19315	<u>L4</u>
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<u>L8</u>	l4 and l7	91	<u>L8</u>
<u>L7</u>	l3 with l6	433	<u>L7</u>
<u>L6</u>	fifo	44490	<u>L6</u>
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<u>L4</u>	l1 near3 l2	19315	<u>L4</u>
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<u>L12</u>	l4 and L11	222	<u>L12</u>
<u>L11</u>	l9 or l10	3981	<u>L11</u>
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<u>L9</u>	711/154-155.ccls.	2007	<u>L9</u>
<u>L8</u>	l4 and l7	91	<u>L8</u>
<u>L7</u>	l3 with l6	433	<u>L7</u>

<u>L6</u>	fifo	44490	<u>L6</u>
<u>L5</u>	l3 and l4	2138	<u>L5</u>
<u>L4</u>	l1 near3 l2	19315	<u>L4</u>
<u>L3</u>	process\$ near2 order	126406	<u>L3</u>
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Inventor Name Search Result

Your Search was:

Last Name = WEBER

First Name = WOLF-DIETRICH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 39
<u>60552576</u>	Not Issued	159	03/11/2004	VARIOUS METHODS AND APPARATUSES FOR WIDTH AND BURST CONVERSION	WEBER, WOLF-DIETRICH
<u>60084795</u>	Not Issued	159	05/08/1998	CACHE COHERENCE UNIT FOR INTERCONNECTING MULTIPROCESSOR NODES HAVING A SPARSE DIRECTORY WITH ENHANCED REPLACEMENT CONTROL	WEBER, WOLF-DIETRICH
<u>11078185</u>	Not Issued	019	03/10/2005	VARIOUS METHODS AND APPARATUS FOR WIDTH AND BURST CONVERSION	WEBER, WOLF-DIETRICH
<u>11077743</u>	Not Issued	020	03/10/2005	VARIOUS METHODS AND APPARATUSES FOR WIDTH AND BURST CONVERSION	WEBER, WOLF-DIETRICH
<u>10963271</u>	Not Issued	030	10/11/2004	METHOD AND APPARATUS FOR SCHEDULING A RESOURCE TO MEET QUALITY-OF-SERVICE RESTRICTIONS	WEBER, WOLF-DIETRICH
<u>10917815</u>	Not Issued	030	08/13/2004	ACCESSING MEMORY AND PROCESSOR CACHES OF NODES IN MULTI-NODE CONFIGURATIONS	WEBER, WOLF-DIETRICH
<u>10917682</u>	Not Issued	020	08/13/2004	RESOLVING CROSSING REQUESTS IN MULTI-NODE CONFIGURATIONS	WEBER, WOLF-DIETRICH
<u>10698905</u>	Not Issued	030	10/31/2003	METHOD AND APPARATUS FOR ESTABLISHING A QUALITY OF SERVICE MODEL	WEBER, WOLF-DIETRICH
<u>10678380</u>	Not Issued	020	10/03/2003	LOW POWER SHARED LINK ARBITRATION	WEBER, WOLF-DIETRICH
<u>10418624</u>	Not Issued	041	04/18/2003	METHOD AND APPARATUS FOR ROUND ROBIN RESOURCE ARBITRATION	WEBER, WOLF-DIETRICH
<u>10418370</u>	Not Issued	030	04/18/2003	VARIOUS METHODS AND APPARATUSES FOR ARBITRATION AMONG BLOCKS	WEBER, WOLF-DIETRICH

				OF FUNCTIONALITY	
<u>10409806</u>	<u>6804757</u>	150	04/08/2003	METHOD AND APPARATUS FOR SCHEDULING REQUESTS USING ORDERED STAGES OF SCHEDULING CRITERIA	WEBER, WOLF-DIETRICH
<u>10330591</u>	Not Issued	030	12/27/2002	METHOD AND APPARATUS FOR AUTOMATIC CONFIGURATION OF MULTIPLE ON-CHIP INTERCONNECTS	WEBER, WOLF-DIETRICH
<u>10288973</u>	Not Issued	061	11/05/2002	METHOD AND APPARATUS FOR CONFIGURABLE ADDRESS MAPPING AND PROTECTION ARCHITECTURE AND HARDWARE FOR ON-CHIP SYSTEMS	WEBER, WOLF-DIETRICH
<u>10285987</u>	Not Issued	030	11/01/2002	METHOD AND APPARATUS FOR ERROR HANDLING IN NETWORKS	WEBER, WOLF-DIETRICH
<u>10144073</u>	Not Issued	030	05/10/2002	SCALABLE LOW BANDWIDTH MULTICAST HANDLING IN MIXED CORE SYSTEMS	WEBER, WOLF-DIETRICH
<u>10138946</u>	Not Issued	030	05/03/2002	COMPOSING ON-CHIP INTERCONNECTS WITH CONFIGURABLE INTERFACES	WEBER, WOLF-DIETRICH
<u>10138772</u>	Not Issued	030	05/03/2002	ON -CHIP INTER-NETWORK PERFORMANCE OPTIMIZATION USING CONFIGURABLE PERFORMANCE PARAMETERS	WEBER, WOLF-DIETRICH
<u>10138284</u>	Not Issued	030	05/03/2002	COMMUNICATION SYSTEM AND METHOD WITH CONFIGURABLE POSTING POINTS	WEBER, WOLF-DIETRICH
<u>09977602</u>	<u>6804738</u>	150	10/12/2001	METHOD AND APPARATUS FOR SCHEDULING A RESOURCE TO MEET QUALITY-OF-SERVICE RESTRICTIONS	WEBER, WOLF-DIETRICH
<u>09977600</u>	Not Issued	061	10/12/2001	METHOD AND APPARATUS FOR SCHEDULING REQUESTS TO A RESOURCE USING A CONFIGURABLE THRESHOLD	WEBER, WOLF-DIETRICH
<u>09977517</u>	<u>6578117</u>	150	10/12/2001	METHOD AND APPARATUS FOR SCHEDULING REQUESTS USING ORDERED STAGES OF SCHEDULING CRITERIA	WEBER, WOLF-DIETRICH
<u>09977510</u>	Not Issued	071	10/12/2001	METHOD AND APPARATUS FOR SCHEDULING OF REQUESTS TO DYNAMIC RANDOM ACCESS MEMORY DEVICE	WEBER, WOLF-DIETRICH
<u>09872126</u>	<u>6785753</u>	150	06/01/2001	METHOD AND APPARATUS FOR	WEBER, WOLF-

				RESPONSE MODES IN A PIPELINED ENVIRONMENT	DIETRICH
<u>09802405</u>	Not Issued	061	03/09/2001	COMMUNICATIONS SYSTEM AND METHOD WITH NON-BLOCKING SHARED INTERFACE	WEBER, WOLF-DIETRICH
<u>09623893</u>	6419736	150	11/13/2000	SULFIDE AND OXYSULPHIDE PIGMENTS	WEBER, WOLF-DIETRICH
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<u>09304432</u>	6625694	150	05/03/1999	SYSTEM AND METHOD FOR ALLOCATING A DIRECTORY ENTRY FOR USE IN MULTIPROCESSOR-NODE DATA PROCESSING SYSTEMS	WEBER, WOLF-DIETRICH
<u>09285316</u>	6490630	150	04/02/1999	SYSTEM AND METHOD FOR AVOIDING DEADLOCK IN A MULT-NODE NETWORK	WEBER, WOLF-DIETRICH
<u>09281714</u>	6560681	150	03/30/1999	SPLIT SPARSE DIRECTORY FOR A DISTRIBUTED SHARED MEMORY MULTIPROCESSOR SYSTEM	WEBER, WOLF-DIETRICH
<u>09003721</u>	6209064	150	01/07/1998	CACHE COHERENCE UNIT WITH INTEGRATED MESSAGE PASSING AND MEMORY PROTECTION FOR A DISTRIBUTED, SHARED MEMORY MULTIPROCESSOR SYSTEM	WEBER, WOLF-DIETRICH
<u>08605677</u>	5959995	150	02/22/1996	ASYNCHRONOUS PACKET SWITCHING	WEBER, WOLF-DIETRICH
<u>08605676</u>	5740346	150	02/22/1996	SYSTEM AND METHOD FOR DYNAMIC NETWORK TOPOLOGY EXPLORATION	WEBER, WOLF-DIETRICH
<u>07338822</u>	4987133	150	04/17/1989	SALICYLIC ACID DERIVATIVES	WEBER, WOLF-DIETRICH
<u>07271463</u>	4950648	150	11/15/1988	ANALGESIC	WEBER, WOLF-DIETRICH
<u>07177279</u>	Not Issued	161	04/04/1988	AMINO ACID DERIVATIVES	WEBER, WOLF-DIETRICH
<u>06784087</u>	Not Issued	168	10/04/1985	(THIO)MORPHOLINES	WEBER, WOLF-DIETRICH
<u>06252841</u>	4357278	250	04/10/1981	PROCESS FOR SYNTHESIZING ESTRONE OR ESTRONE DERIVATIVES	WEBER, WOLF-DIETRICH

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Mitchell, P.D.; Grace, D.; Tozer, T.C.;
Personal, Indoor and Mobile Radio Communications, 2001 12th IEEE International Symposium c
Volume 2, 30 Sept.-3 Oct. 2001 Page(s):F-90 - F-94 vol.2
[AbstractPlus](#) | Full Text: [PDF](#)(415 KB) IEEE CNF
- ☐ 2. **Request combining in multiprocessors with arbitrary interconnection networks**
Lebeck, A.R.; Sohi, G.S.;
Parallel and Distributed Systems, IEEE Transactions on
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Abdul-Fatah, I.; Majumdar, S.;
Parallel and Distributed Systems, IEEE Transactions on
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- ☐ 4. **On reducing hotspot contention in multistage networks**
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Signals, Systems and Computers, 1991. 1991 Conference Record of the Twenty-Fifth Asilomar
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[AbstractPlus](#) | Full Text: [PDF](#)(288 KB) IEEE CNF
- ☐ 5. **An effective synchronization network for large multiprocessor systems**
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Parallel Processing Symposium, 1991. Proceedings., Fifth International
30 April-2 May 1991 Page(s):309 - 317
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- ☐ 6. **A negotiation model for decision making in power system planning**
Wang, S.-M.; Liu, C.-C.;
Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE International Symposium on
Volume 5, 3-6 May 1992 Page(s):2549 - 2552 vol.5
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- ☐ 7. **The Constraint-Based Knowledge Broker system**
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8. Comparative performance of the CFDMA protocol via satellite with various terminal requirements

Mitchell, P.D.; Grace, D.; Tozer, T.C.;
Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE
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9. A scalable on-demand video delivery paradigm

Su-Jun Ma; Min You Wu; Wei Shu;
Multimedia and Expo, 2002. ICME '02. Proceedings. 2002 IEEE International Conference on
Volume 1, 26-29 Aug. 2002 Page(s):17 - 20 vol.1

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Evolutionary Computation, 2002. CEC '02. Proceedings of the 2002 Congress on |

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Thaler, M.; Moschytz, G.S.;
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- ☐ **9. Unified framework for optimal video streaming**
de Cuetos, P.; Ross, K.W.;
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Wu, M.; Sun, X.-H.;
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- ☐ **11. Power constrained high-level synthesis of battery powered digital systems**
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- ☐ **12. On the combined scheduling of malleable and rigid jobs**
Hungershofer, J.;
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- ☐ **13. Combined scheduling and data routing for programmable ASIC systems**
Hartmann, R.;
Design Automation, 1992. Proceedings. [3rd] European Conference on
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- ☐ **14. Emission oriented production control strategies based on fuzzy expert systems, neural networks and neuro-fuzzy approaches**
Tuma, A.; Haasis, H.-D.; Rentz, O.;
Neural Networks, 1993. IJCNN '93-Nagoya. Proceedings of 1993 International Joint Conference
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- ☐ **15. Achievability of combined GOS requirements in broadband networks**
Jeon, Y.-H.; Viniotis, I.;
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- ☐ **16. ILP synthesis of signal processing architectures with minimum structural complexity**
Haroun, B.; Sajjadi, B.;
Custom Integrated Circuits Conference, 1994., Proceedings of the IEEE 1994
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- ☐ **17. Scheduling strategies for low-energy programmable digit-serial Reed-Solomon codecs**
Song, L.; Parhi, K.K.;
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8-10 Oct. 1998 Page(s):275 - 284
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- ☐ **18. Program graph scheduling in the look-ahead reconfigurable multiprocessor system**
Laskowski, E.;
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13-16 March 2001 Page(s):791 - 796
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- ☐ **20. A low power scheduler using game theory**
Ranganathan, N.; Murugavel, A.K.;
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- ☐ **21. GA approach to scheduling of generator units**
Wong, Y.K.; Chung, T.S.; Tuen, K.W.;
Advances in Power System Control, Operation and Management, 2000. APSCOM-00. 2000
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